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International application number: PCT/US05/008892

International filing date: 16 March 2005 (16.03.2005)

Document type: Certified copy of priority document

Document details: Country/Office: US
Number: 60/554,258
Filing date: 17 March 2004 (17.03.2004)

Date of receipt at the International Bureau: 02 May 2005 (02.05.2005)

Remark: Priority document submitted or transmitted to the International Bureau in compliance with Rule 17.1(a) or (b)



World Intellectual Property Organization (WIPO) - Geneva, Switzerland
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1311366

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APPLICATION NUMBER: 60/554,258

FILING DATE: *March 17, 2004*

RELATED PCT APPLICATION NUMBER: *PCT/US05/08892*



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PROVISIONAL APPLICATION COVER SHEET

CERTIFICATE OF EXPRESS MAILING

I hereby certify that this paper and the documents and/or fees referred to as attached herein are being deposited with the United States Postal Service on March 10, 2004 in an envelope as "Express Mail Post Office to Addressee" service under 37 CFR §1.10, Mailing Label Number EV 333 985 323 US, addressed to the Commissioner for Patents, Mail Stop Provisional Patent Application, P.O. Box 1450 Alexandria, VA 22313-1450.

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2264 U.S. PTO
60/554258
031704

Mail Stop Provisional Patent Application
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Sir: This is a request for filing a PROVISIONAL APPLICATION under 37 CFR 1.53(c).

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TITLE OF INVENTION (280 characters max)

ELECTRICAL CONNECTOR FOR SEMICONDUCTOR DEVICE TEST FIXTURE AND TEST ASSEMBLY

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ENCLOSED APPLICATION PARTS (check all that apply)

- ☒ Specification Number of Pages 3 ☒ Other (specify) Provisional Cover Sheet
- ☒ Drawing(s) Number of Sheets 1
- ☐ Assignment and Assignment Recordation Cover Sheet (recording fee of \$40.00 enclosed)

- ☒ Applicant is entitled to Small Entity Status under 37 C.F.R. § 1.27.
- ☒ A check or money order is enclosed to cover the Provisional filing fees. Provisional Filing Fee Amount \$80.00
- ☒ The commissioner is hereby authorized to charge any additional fees which may be required or credit any overpayment to Deposit Account No. 500388 (Order No. QLTIP012P).

Respectfully Submitted,

SIGNATURE

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PROVISIONAL APPLICATION FILING ONLY

PROVISIONAL PATENT APPLICATION

**ELECTRICAL CONNECTOR FOR SEMICONDUCTOR DEVICE TEST
FIXTURE AND TEST ASSEMBLY**

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Provisional Application Draft

**TEST FIXTURES AND CONNECTIVITY METHOD WITH TRUE SENSING
AND GUARDED SIGNALS ALL THE WAY TO THE DEVICES-UNDER-TEST**

(by Peter P. Cuevas)

Introduction

The electric wires connecting between the Device(s) Under-Test (DUT(s)) and the electronic equipment, which delivers the various electrical stimuli to the DUTs and measure them accordingly, require special attention, in particular when sensitive semiconductor and/or passive devices are involved. Since the DUTs are commonly placed on a special fixture (hereinafter "Test Fixture"), these connecting wires may develop non-negligible Ohmic voltage drop due the flowing current, as well as leakage to their surrounding (neighboring wires in particular). The stringent demands from state-of-the-art electronic device testing, in terms of accuracy, sensitivity and low-leakage current, over a wide range of current and voltage levels, make such connectivity issues both important and challenging.

The most common approach to address these concerns is by using two tri-axial cables (hereinafter "Triax") per each electronic stimulus, where both true-sensing and minimal leakage are important. The center conductor of one cable is the forcing signal, the surrounding cylindrical conducting shell is the guarding signal ("Guard"), driven by the same potential as the forcing signal, but insulated both electrically and physically from it. The outer metal shell is usually connected to ground potential for safety and also shields from outside noise (RFI). Similarly, the center conductor of the other Triax delivers the sensed signal to the high impedance measurement unit, with a similar guarding scheme and outer metal shell. Since the force and sense lines are fully enclosed by their surrounding Guards, all with almost the same potential, the leakage is reduced significantly. Also, connecting the sense to the DUT assures measurement of the required voltage at the DUT, rather than the forcing signal, possibly affected by Ohmic losses along its connecting line.

As this technique is well known and documented in the prior art (for example, Agilent Technologies 4155B/4156B Semiconductor Parameter Analyzer User's Guide General Information, page 2-38), it is obviously beyond the scope of this application. However, even with such two-Triax approach, there is a problem with the final connection within the Test Fixture. As each Triax terminates with a respective connector on the Test Fixture, the connection from this connector to the DUT is implemented with simple wires due to physical constraints (see Fig. 1). Furthermore, to facilitate connectivity to every possible pin of the DUT, a "jumper matrix", made off plug-in wires connecting between the signals and their intended destination, is needed. In all, the guarding scheme is practically broken at the connectors that terminate the Triax cables at the Test Fixture and not as close as possible to the DUT.

The following invention provides a solution to this problem, by different cables, well suited for such task, and an overall simplification of the connectivity scheme. It also

introduces a new test fixture mounted as a rotating tray, which can serve as front cover to the electronic equipment as needed. This eliminates the long Triax cables and the separate and remote Test Fixture altogether, while still providing the improved connectivity scheme of the invention.

Summary of the Invention

The first element in the invention is the use of miniature USB cables, very common in computer related applications. These flexible, relatively cheap, readily available and physically small cables possess excellent insulation characteristics. Each cable contains five inner wires, all surrounded by a metallic enclosure. The five inner-wires are used as follows: One wire for Sense, one wire for Guard and the remaining three wires for Force. Note that three wires are allocated for the force signal to maximize its current carrying capability; however, any combination which assures at least one dedicated wire to each of the three functions (namely, Force, Sense, Guard) is applicable and should be considered an integral part of this invention. Another related provision addresses safe handling: The outer coating along the entire USB cable is made of flexible insulator. Its only exposed part is the terminating edge which requires plug-in connection to a mating connector in the Test Fixture and, in the opposite end, to the electronic equipment via similar connector. Since these cable edges are exposed to allow good electrical contact between the outer metal and the mating connector's frame, the user may be exposed to the voltage at this outer metal before the cable is plugged into its mating connector (once plugged, everything is insulated). As the outer metal provides good noise protection and prevents leakage when used as Guard, connecting it to the Guard signal is desirable, so this safety issue must be properly addressed.

The solution is described in Fig. 2: The outer metal is not connected to any signal as long as the cable is not plugged into the Test Fixture. Once plugged, an internal connection on the insulated printed circuit board of the Test Fixture effectively shorts between the dedicated inner wire of the USB cable, carrying the Guard signal, and the outer metal. This way, only when the cable is fully plugged and the user is not exposed to its conductive parts, the outer metal is fully guarded as required.

Since the USB cable is small and its connector presents such a small foot-print, we are able to connect as many cables as needed for practical testing and well beyond that (28 for example, with straightforward expansion to 40). This eliminates the need for such "jumper matrix", as shown in Fig. 2. Furthermore, a single printed circuit delivers each and every signal to its intended DUT pin with fully guarded lines, all the way to the DUT(s).

Finally, with the simplifications described above, a novel Test Fixture is added as part of this invention. A cover to the front panel is modified to house a complete Test Fixture, with the USB wires connecting directly between the electronic box (its front panel) and the Test Fixture. This eliminates the Triax cables altogether, reduces the length of any connecting cable from several feet to about 10 inches and allows, by a handy rotation scheme, testing while the Test Fixture is open (perpendicular to the front panel), or fully closing the electronic equipment as front cover (DUTs inside).

CLAIMS

1. For use in connection a semiconductor device under test (DUT) having a plurality of leads to electronic test equipment, an interconnect assembly comprising:

a) a cable including a plurality of wires with at least one wire for sensing a signal from a DUT, at least one wire for a forcing signal to the DUT, and at least one wire for a guarding signal driven by the same electrical potential as the forcing signal,

b) a male connector including the plurality of wires, an outer metal coating surrounding the plurality of wires, and an insulating coating around the outer metal coating, and

c) a receptacle connector for receiving the male connector and plurality of wires with corresponding contacts.

2. The interconnect assembly as defined by claim 1 wherein the receptacle connector includes a metal housing, one contact of the receptacle connector for receiving the wire for a guarding signal being electrically connected to the metal housing of the receptacle connector.

3. The interconnect assembly as defined by claim 2 wherein the cable and the male connector include a plurality of wires for a forcing signal.

4. The interconnect assembly as defined by claim 2 wherein the receptacle connector is mountable on a printed circuit board of a test fixture with printed wiring of the printed circuit board connecting contacts of the receptacle connector to a socket of the test fixture.

5. The interconnect assembly as defined by claim 4 wherein the test fixture is housed within electronic test equipment.

